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active region applied approximately assume atom base current bias biasing binary Bode plot

capacitance capacitor charge circuit of Fig collector current collector junction common-emitter concentration conduction configuration constant crystal current gain curve cutin cutoff decreases Define density device diffusion electrons emitter junction energy epitaxial equal equation equivalent circuit flip-flop forward-biased germanium given Hence hole impedence impurity increases indicated in Fig input resistance integrated circuit inverter linear load line logic magnitude metal minority carriers MOSFET n-p-n transistor n-type negative obtained open-circuited operational amplifier oscillator output resistance output voltage p-n junction p-n-p transistor parameters plot poles positive potential pulse rectifier Repeat Rev represents resistor result reverse-biased saturation current semiconductor shown in Fig silicon sinusoidal small-signal stage substrate switch temperature terminal transfer function truth table volt-ampere volt-ampere characteristic voltage gain waveform zero

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